



PARSEC vs. SPLASH-2: A Quantitative Comparison of Two Multithreaded Benchmark Suites

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Outline



Overview

- What is PARSEC?
- Why a new benchmark suite?

Objectives of PARSEC

- Technology Trend 1: Proliferation of CMPs
- Technology Trend 2: Change of Technology Constraints
- Technology Trend 3: Growth of World Data

Characteristics Analysis

- Methodology
- Results

Conclusions

What is PARSEC?



- Princeton <u>Application Repository for Shared-Memory Computers</u>
- Benchmark Suite for Chip-Multiprocessors
- Started as Joint-Venture between Intel and Princeton University
- Freely available at:

http://parsec.cs.princeton.edu/

You can use it for your research

Requirements for a Benchmark Suite for CMPs



- Multithreaded Applications

 Future programs must run on multiprocessors
- Emerging Workloads
 Increasing CPU performance enables new applications
- Diverse

 Multiprocessors are being used for more and more tasks
- State-of-Art Techniques
 Algorithms and programming techniques evolve rapidly
- Support Research
 Our goal is insight, not numbers

Assessment of Situation



	Multithreaded	Emerging Workloads	Diverse	Not HPC- Focused	Research
SPEC CPU2006	No	No	Yes	No	No
SPEC OMP2001	Yes	No	Yes	No	No
SPLASH-2	Yes	No	Yes	No	Yes
ALPBench	Yes	Yes	No	Yes	Yes
BioBench	No	No	No	No	Yes
BioParallel	Yes	No	No	No	Yes
MediaBench II	No	No	No	Yes	Yes
MineBench 2.0	Yes	No	No	Yes	Yes
PhysicsBench	Yes	Yes	No	Yes	Yes

This is why we created PARSEC

Workloads



Program	Application Domain	Parallelization	
Blackscholes	Financial Analysis	Data-parallel	
Bodytrack	Computer Vision	Data-parallel	
Canneal	Engineering	Unstructured	
Dedup	Enterprise Storage	Pipeline	
Facesim	Animation	Data-parallel	
Ferret	Similarity Search	Pipeline	
Fluidanimate	Animation	Data-parallel	
Freqmine	Data Mining	Data-parallel	
Streamcluster	Data Mining	Data-parallel	
Swaptions	Financial Analysis	Data-parallel	
Vips	Media Processing	Data-parallel	
X264	Media Processing	Pipeline	

PARSEC is substantially different from SPLASH-2

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Objectives of PARSEC



- PARSEC was designed to capture recent technology trends:
 - Proliferation of CMPs
 Multiprocessors are used in more and more areas
 - Change of Technology Constraints
 Different software optimizations required for CMPs
 - Growth of World Data

 Huge increase of stored data which must be processed
- These trends are changing programs

Impact of Technology Trends



Proliferation of CMPs:

- New application areas (e.g. video games)
- New parallelization models (e.g. pipelining)

Change of Technology Constraints:

- Constrained off-chip bandwidth
- Shared caches

Growth of World Data:

- Huge increase of input data
- Higher importance of linear algorithms

We show that these trends affect program characteristics

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Methodology

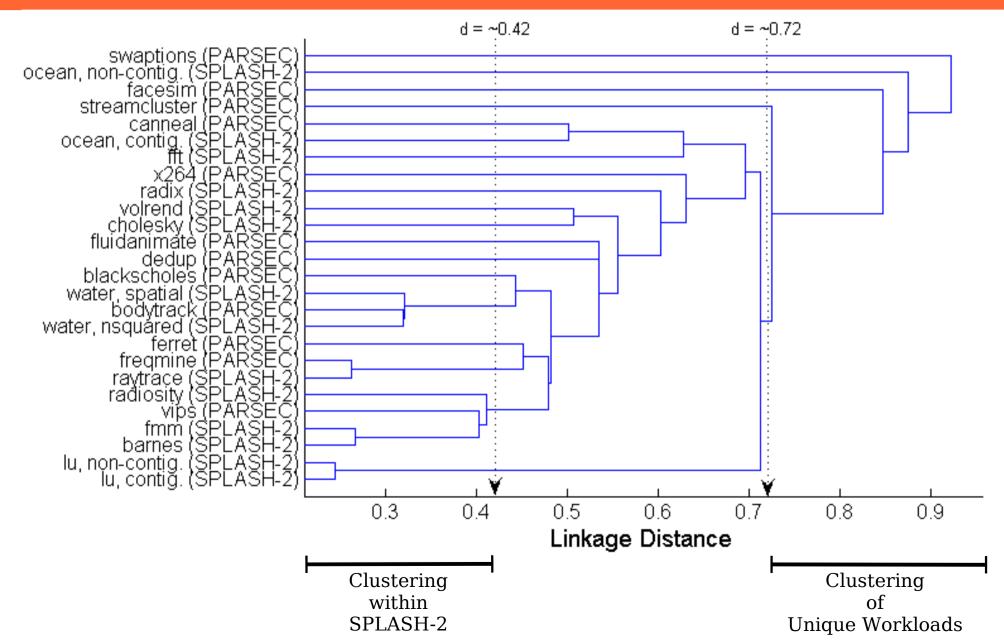


- Simulate abstract cache hierarchy with CMP\$im
- Preprocess chosen characteristics with Principal Component Analysis (PCA) to eliminate correlation
- Compute similarity with hierarchical clustering
- Visualize results with dendrograms and scatter plots

- 44 characteristics chosen:
 - Instruction mix(4 characteristics)
 - Working set(8 characteristics)
 - Sharing(32 characteristics)

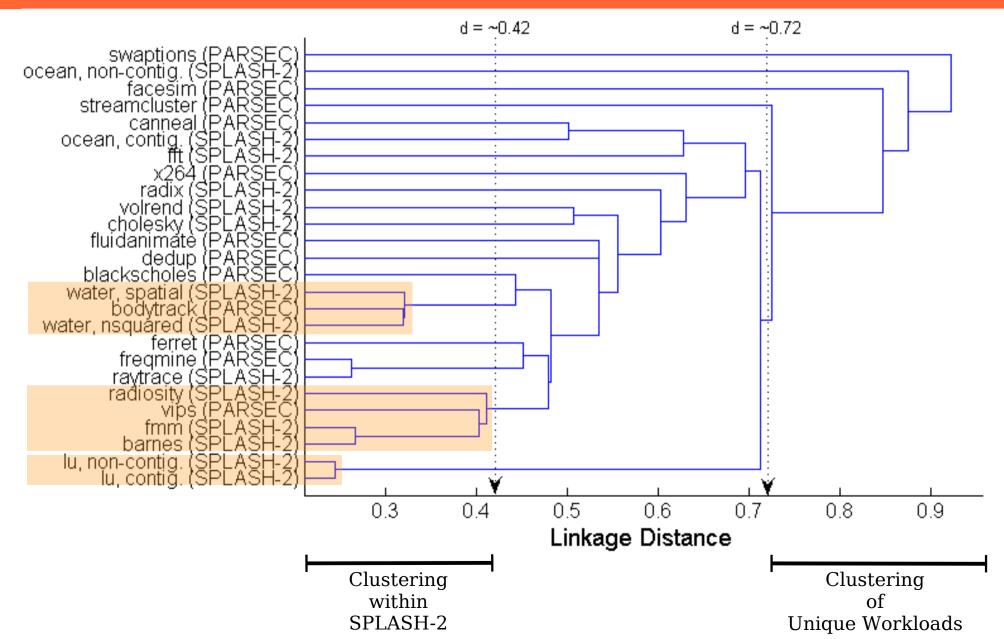
Redundancy & Similarity





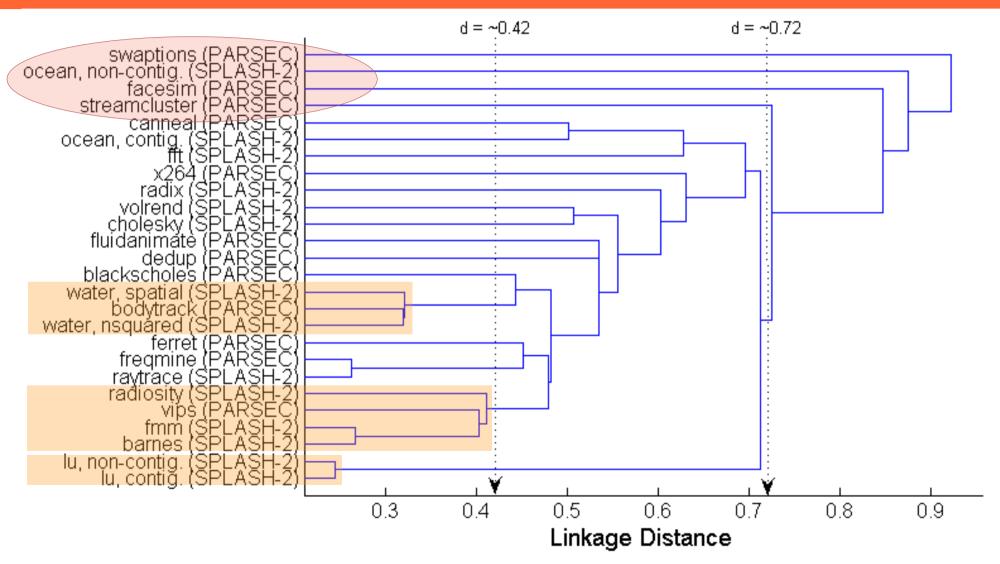
Redundancy & Similarity





Redundancy & Similarity

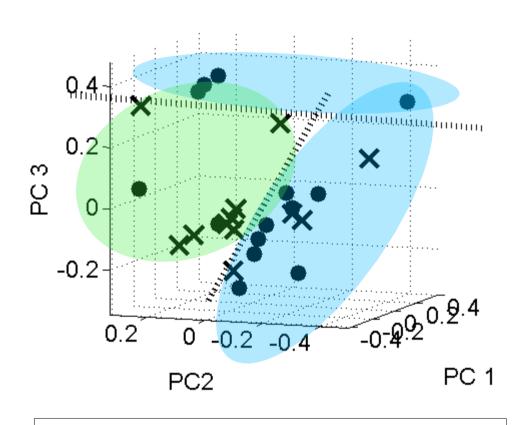




PARSEC is more diverse than SPLASH-2

Systematic Differences





Benchmark suites cluster in different areas, little overlap

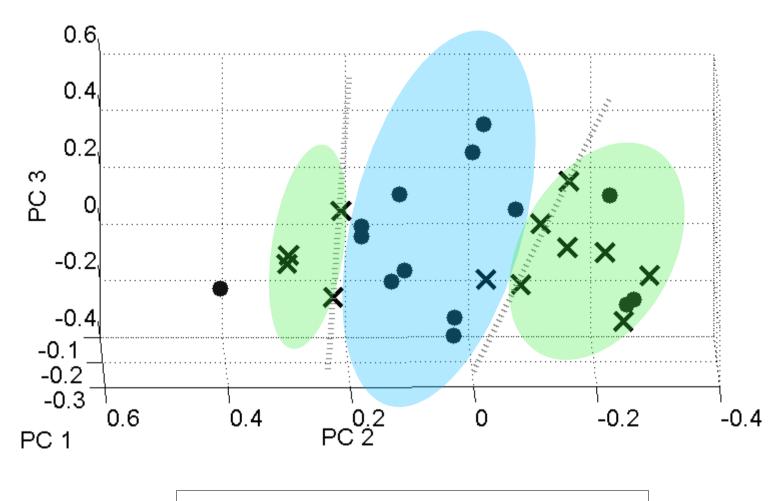
× PARSEC

• SPLASH-2

PARSEC and SPLASH-2 have little in common

Instruction Mix Differences





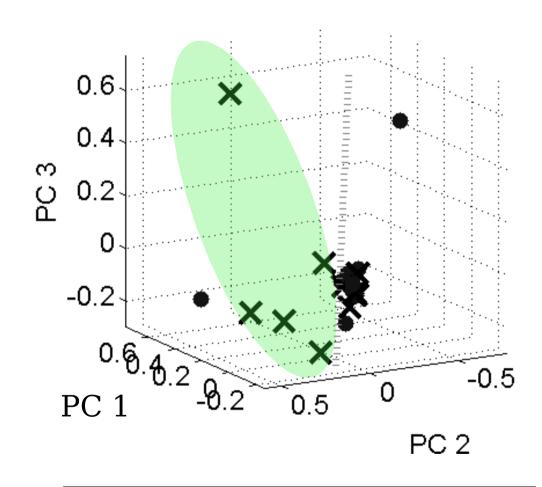
× PARSEC

• SPLASH-2

PARSEC workloads use cores differently

Working Set Differences





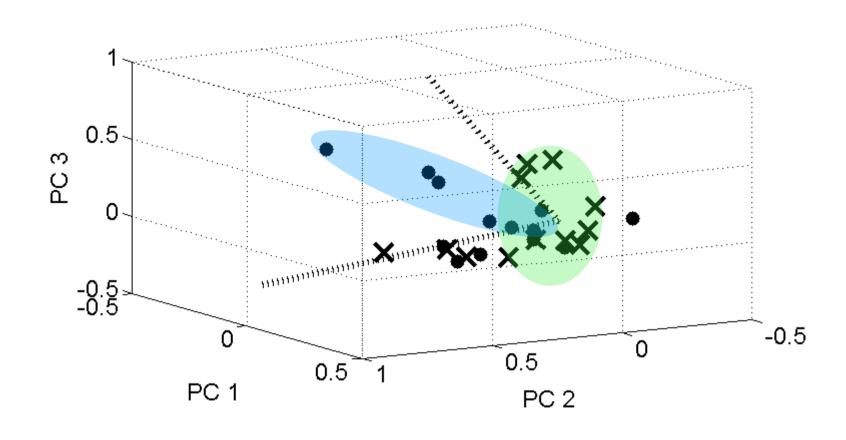
× PARSEC

• SPLASH-2

Some PARSEC workloads use memory differently

Sharing Behavior Differences





× PARSEC

• SPLASH-2

PARSEC workloads communicate differently

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 PARSEC and SPLASH-2 are substantially different benchmark suites

PARSEC is more diverse

No single reason for differences





Thank you!

Questions?